

FIG. 1

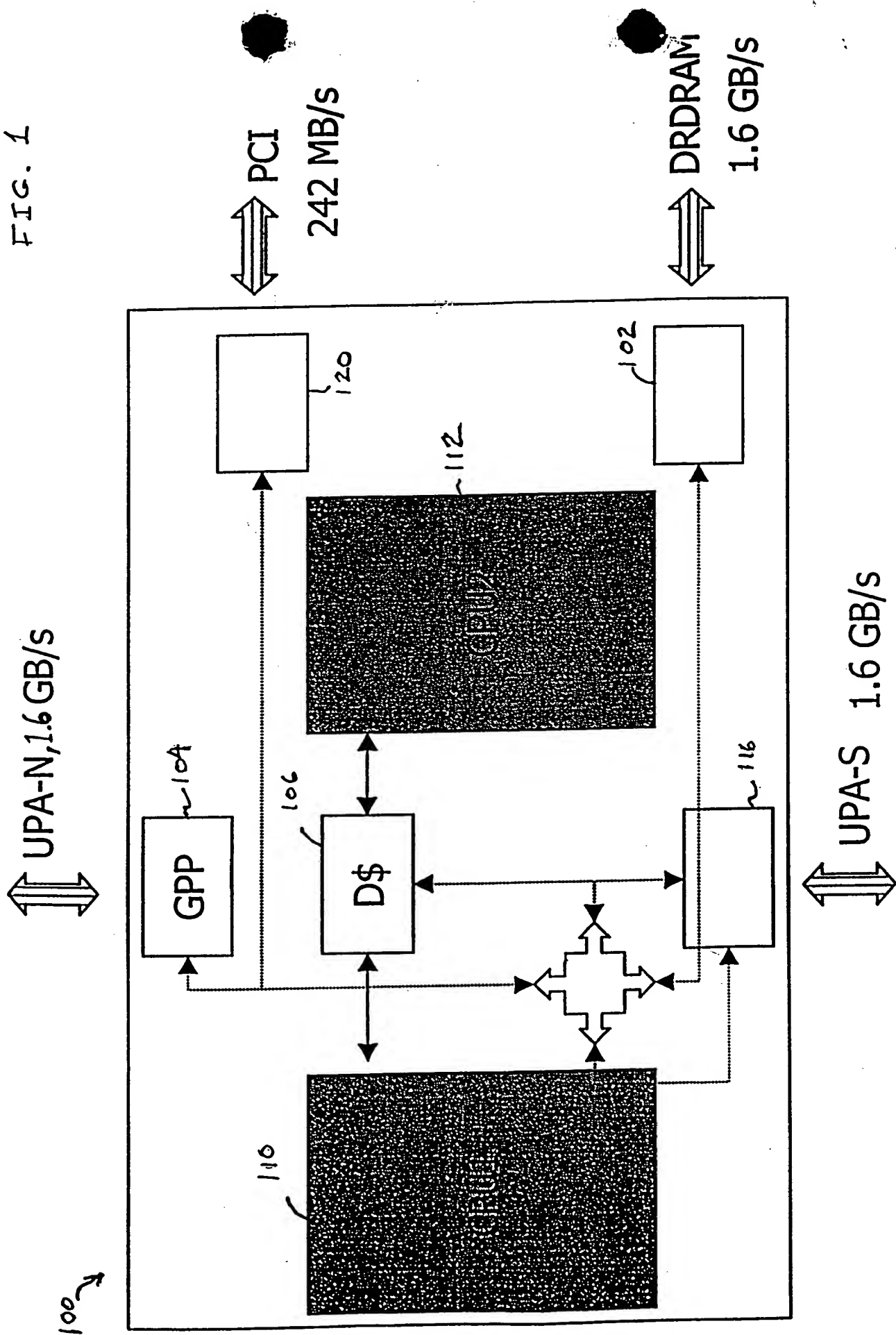
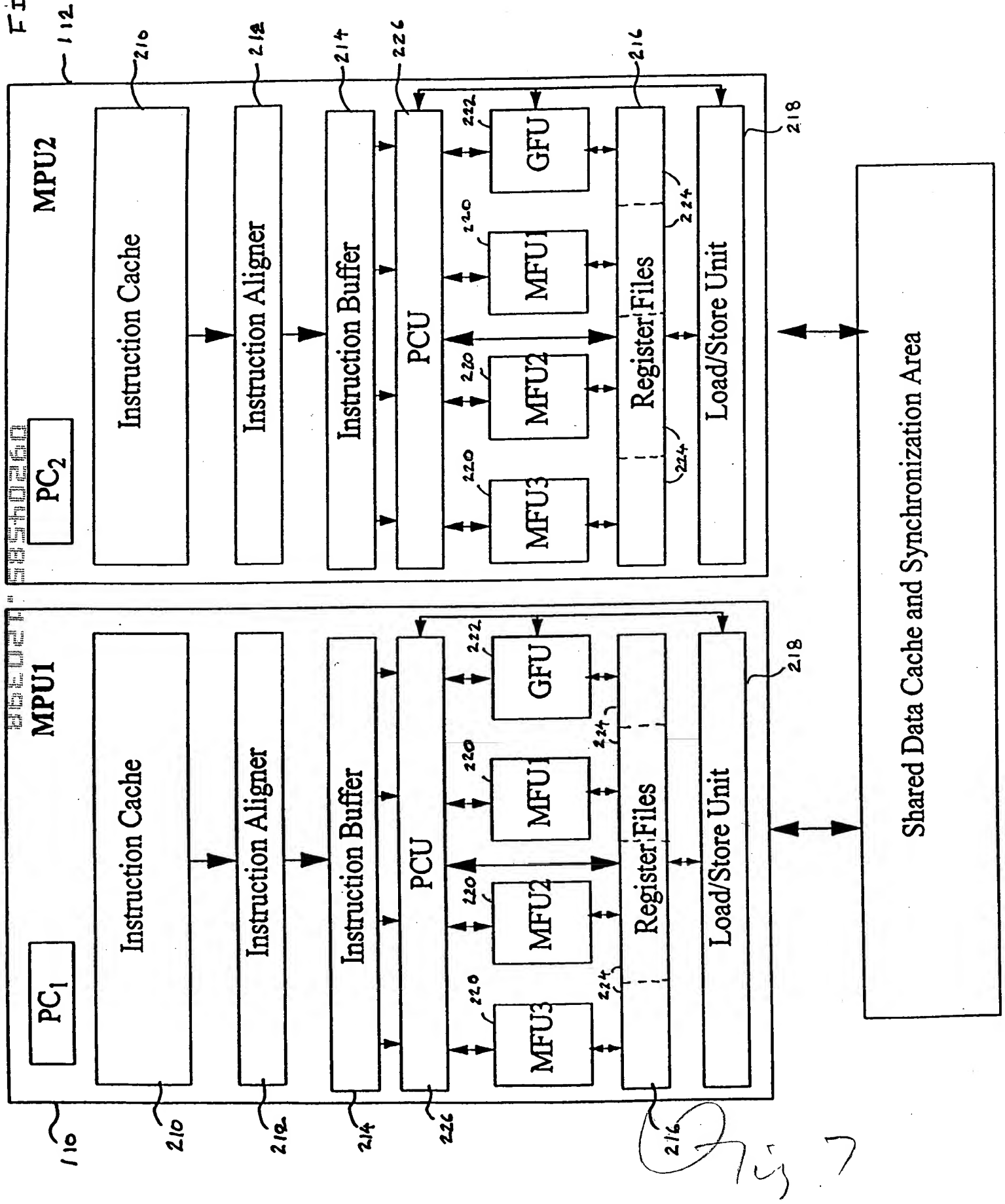


FIG. 2



216
↙

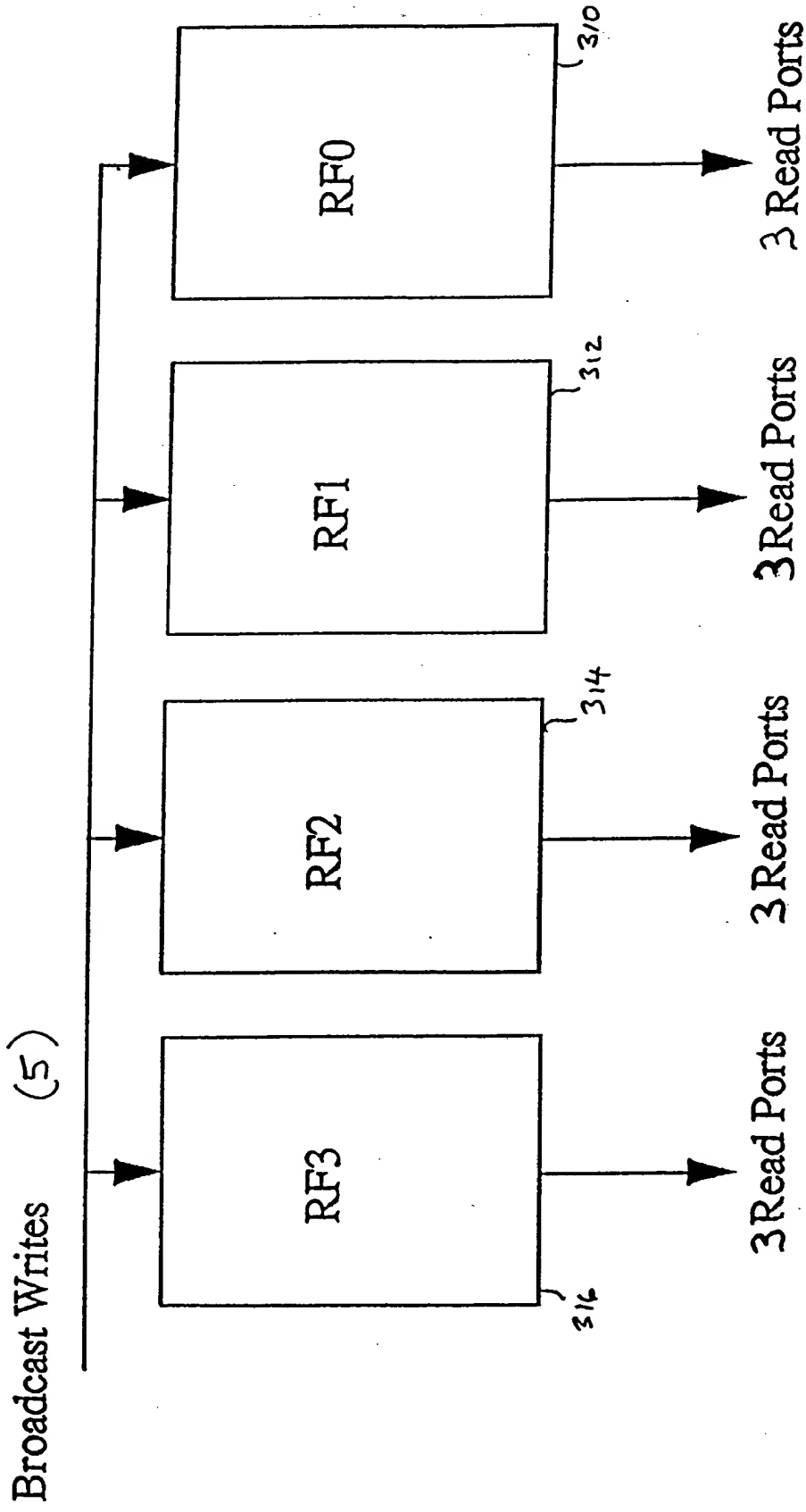


FIGURE 3 .Logical register file physically split in four coherent parts.

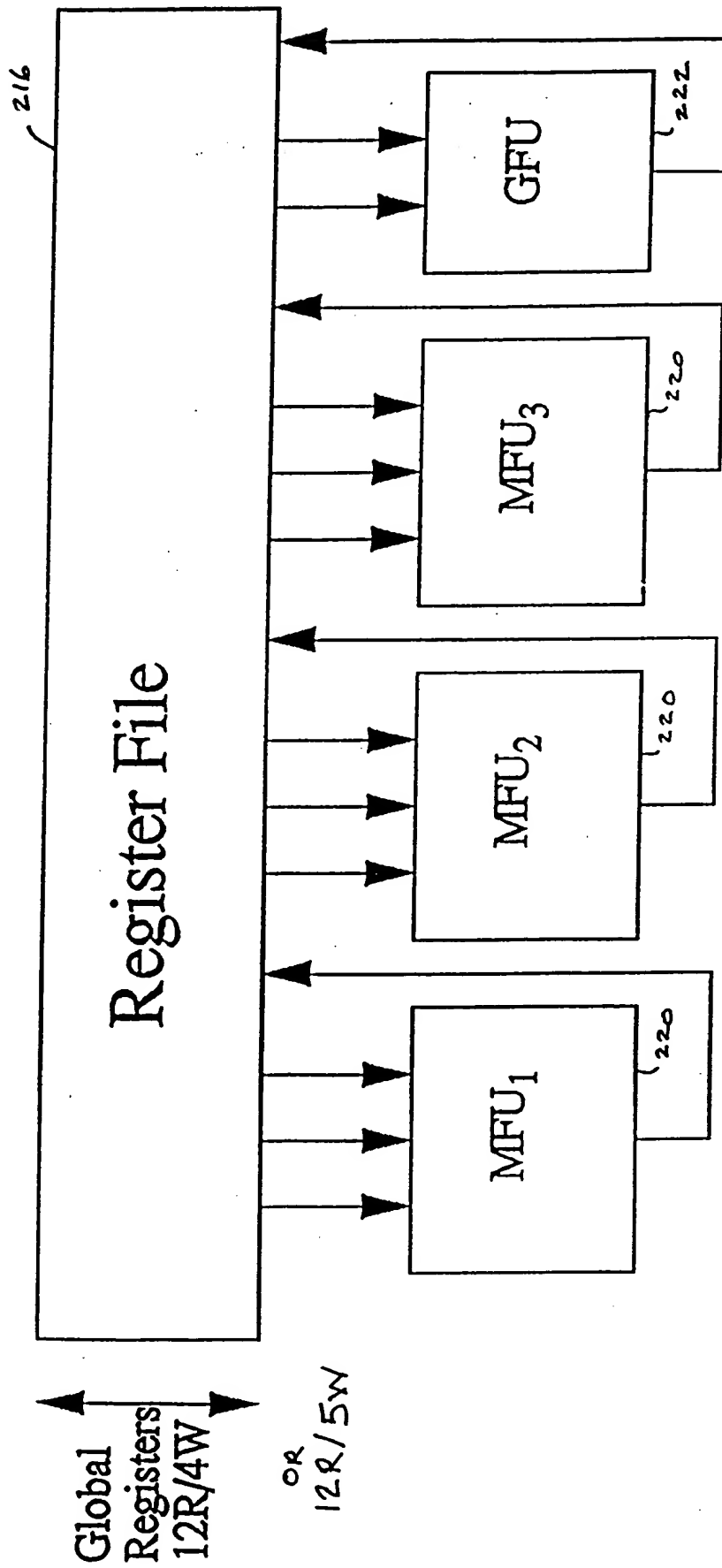
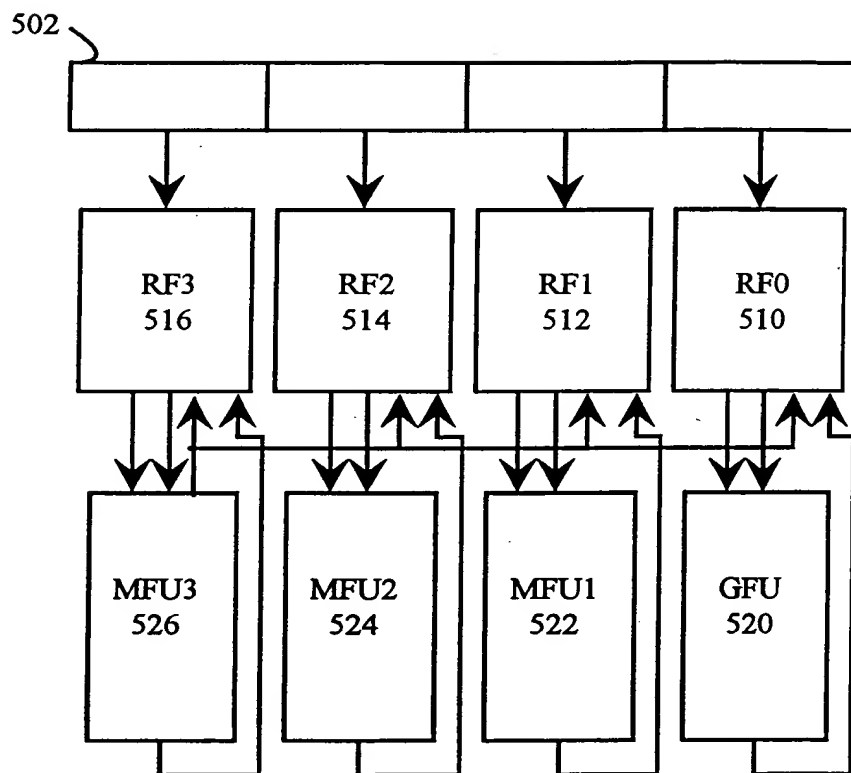


FIGURE 4 .Logical view of the register file to the compiler/programmer.

FIG. 5A



muladd Ra, Rb, Rc, Rd

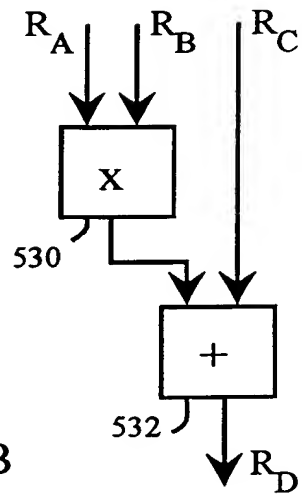


FIG. 5B

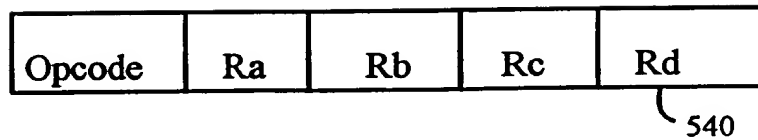
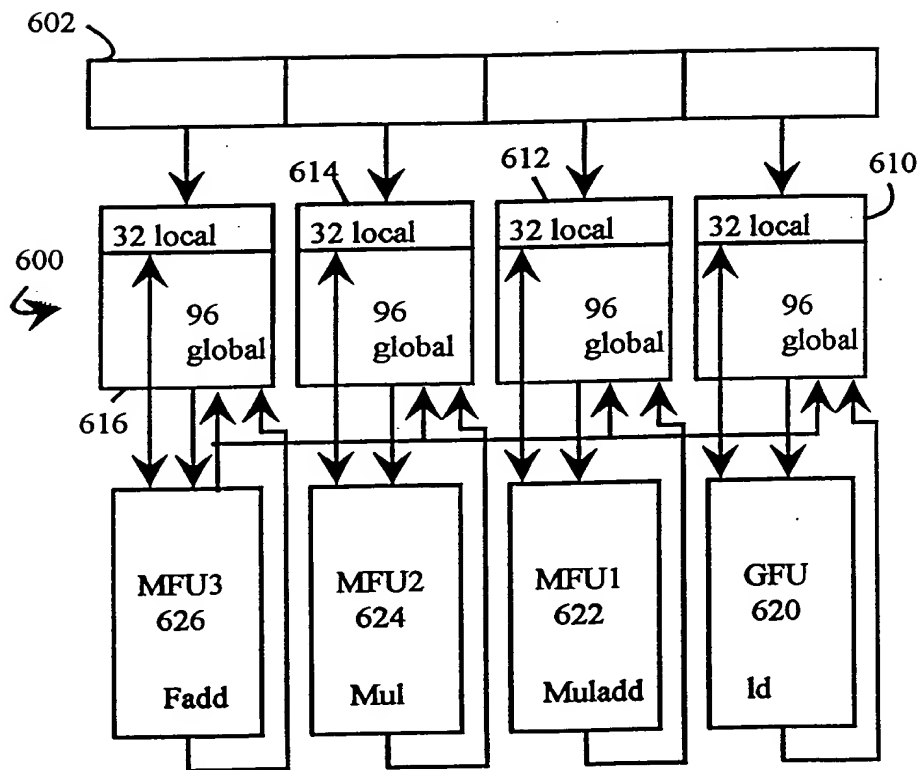


FIG. 5C

FIG. 6



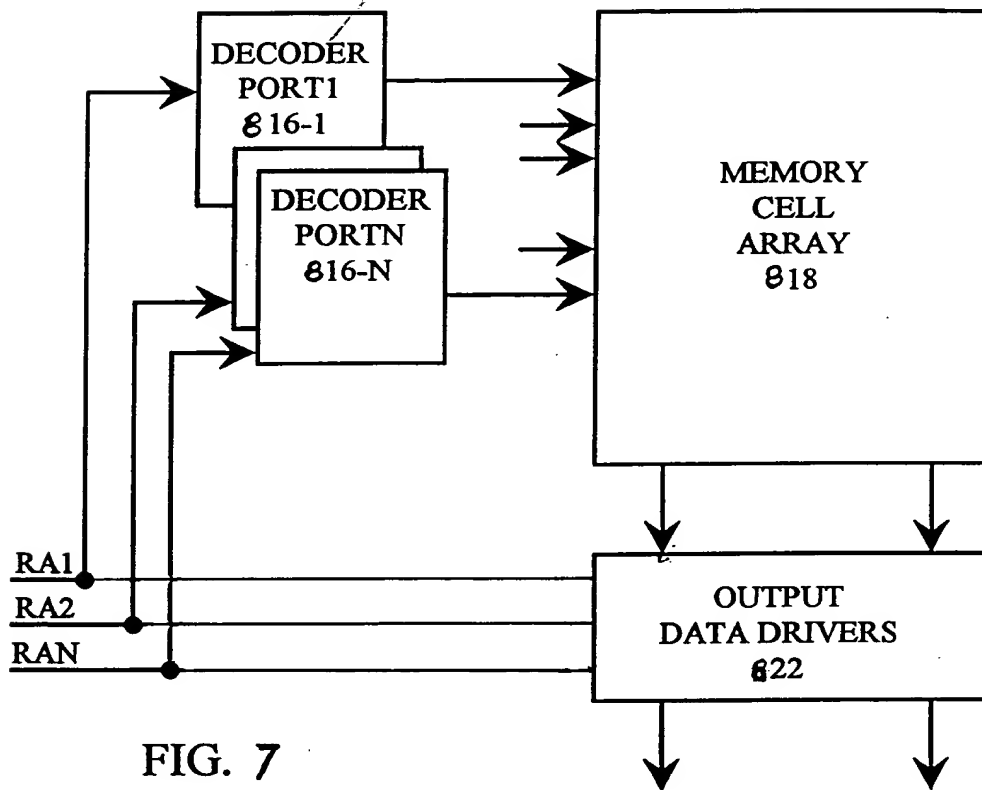
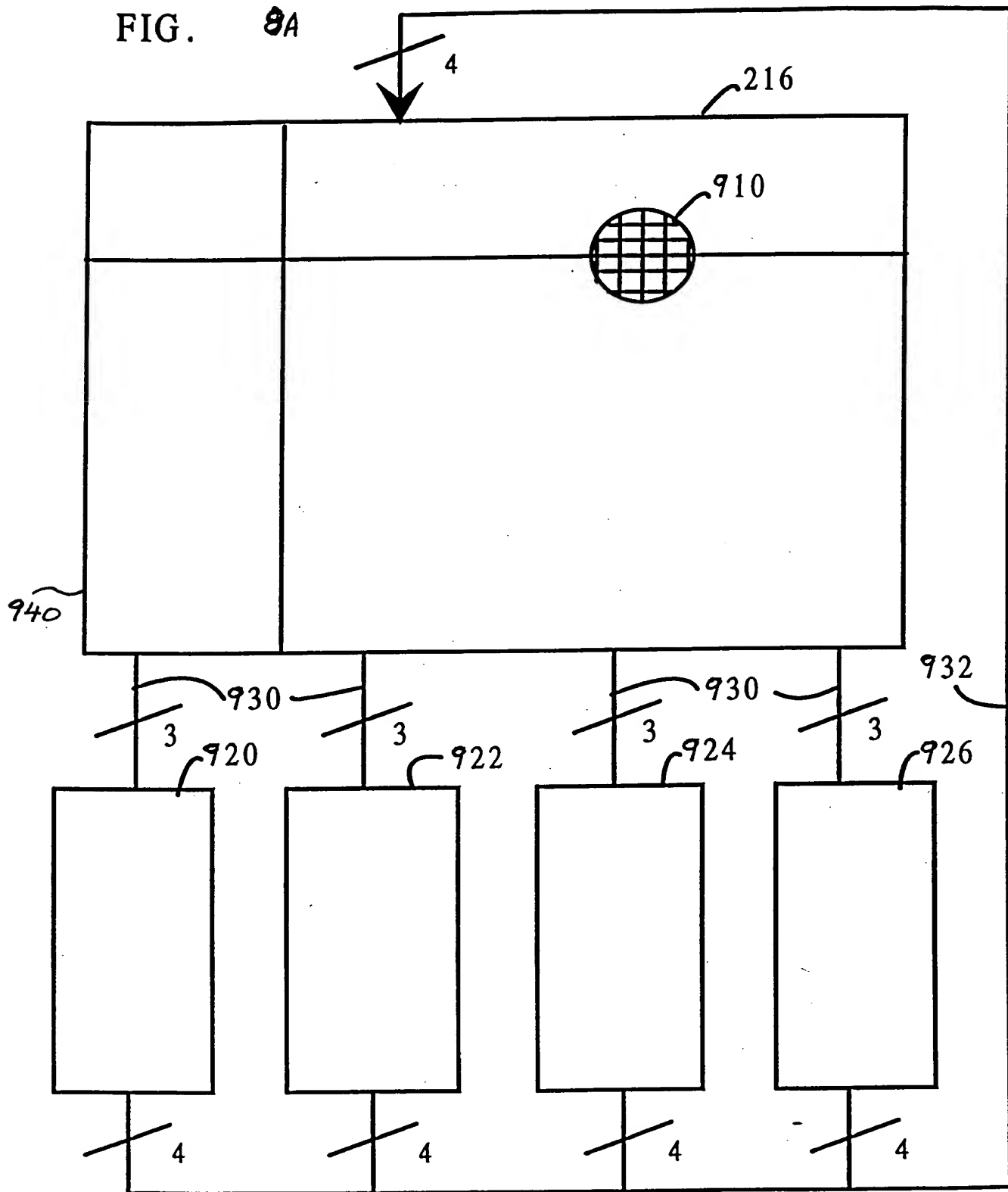


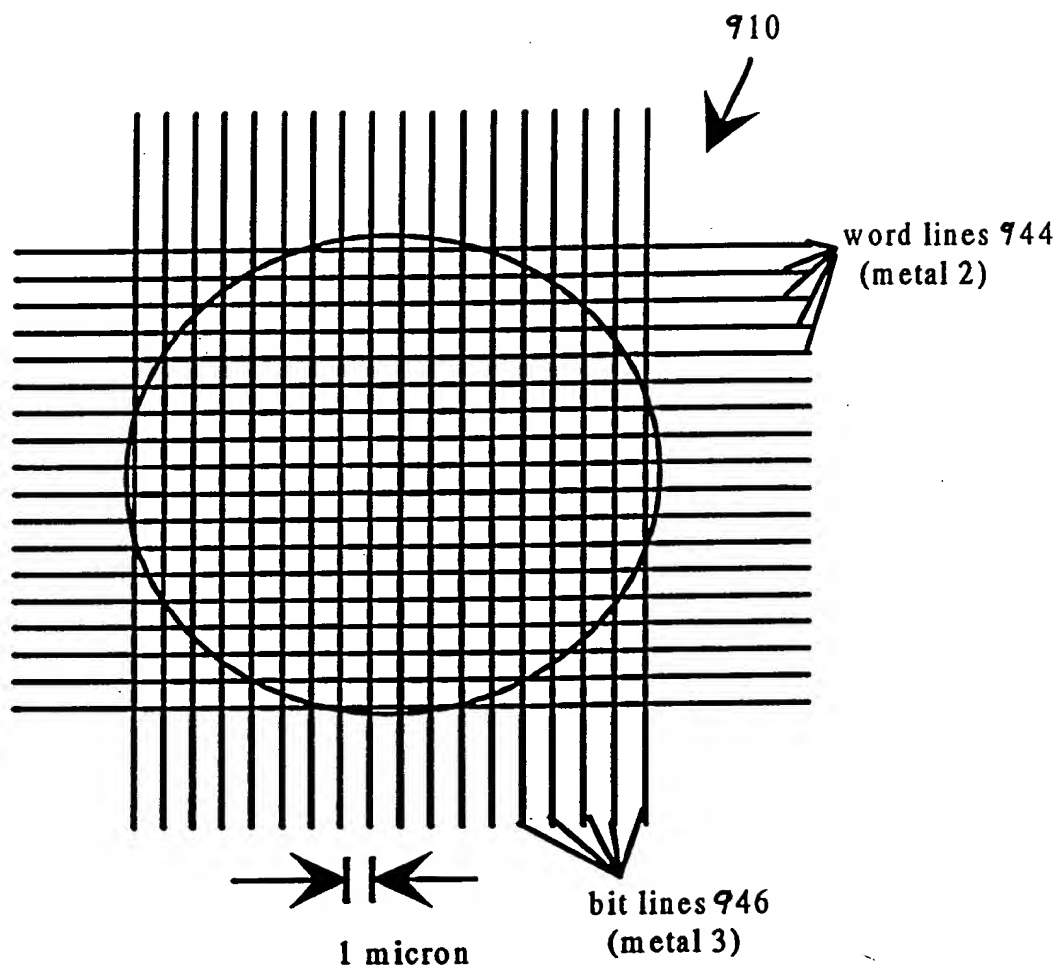
FIG. 7

FIG. 8A



0904585 120399

FIG. 8B



09204585 10098 86027 58540260

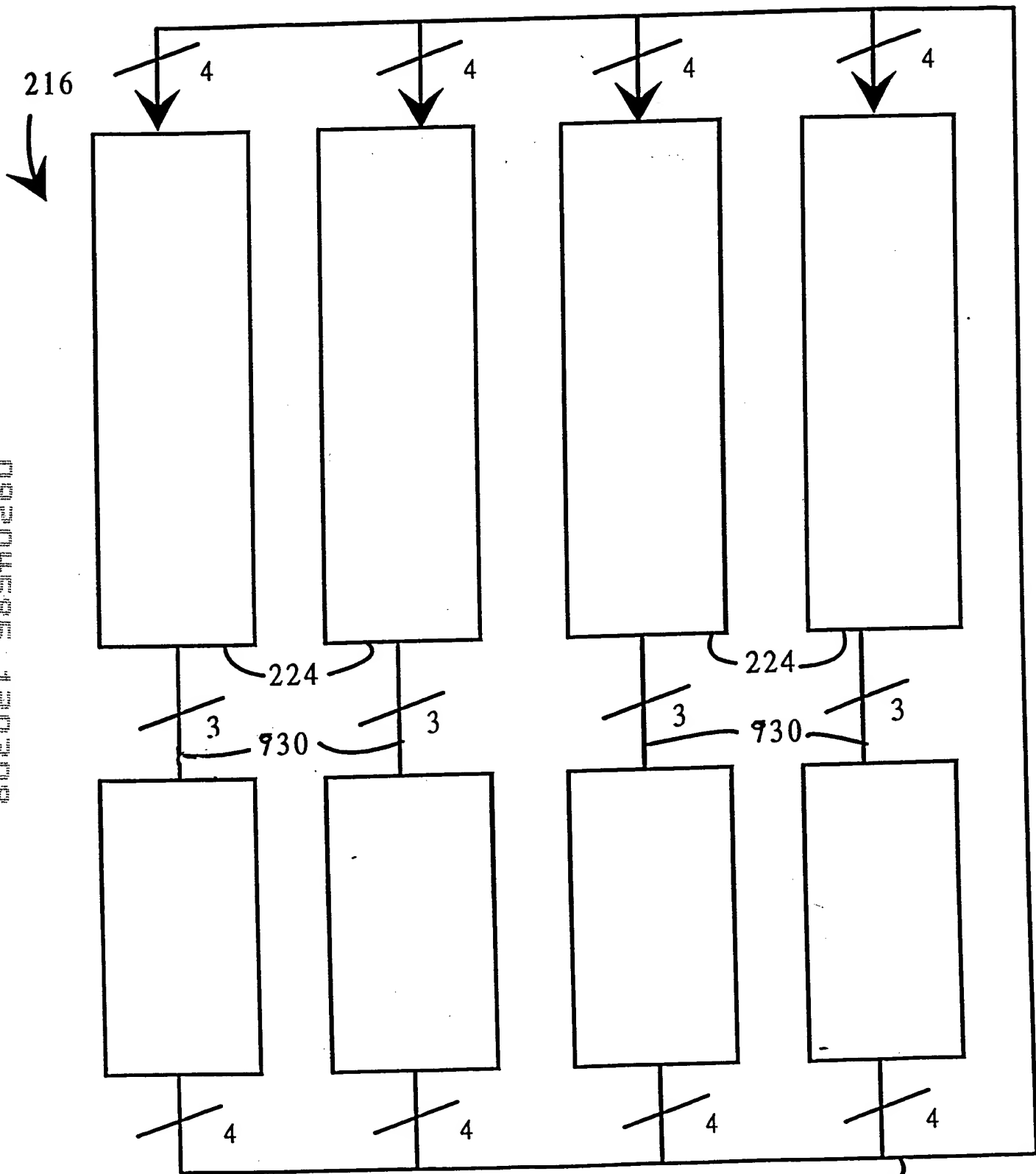


FIG. 9

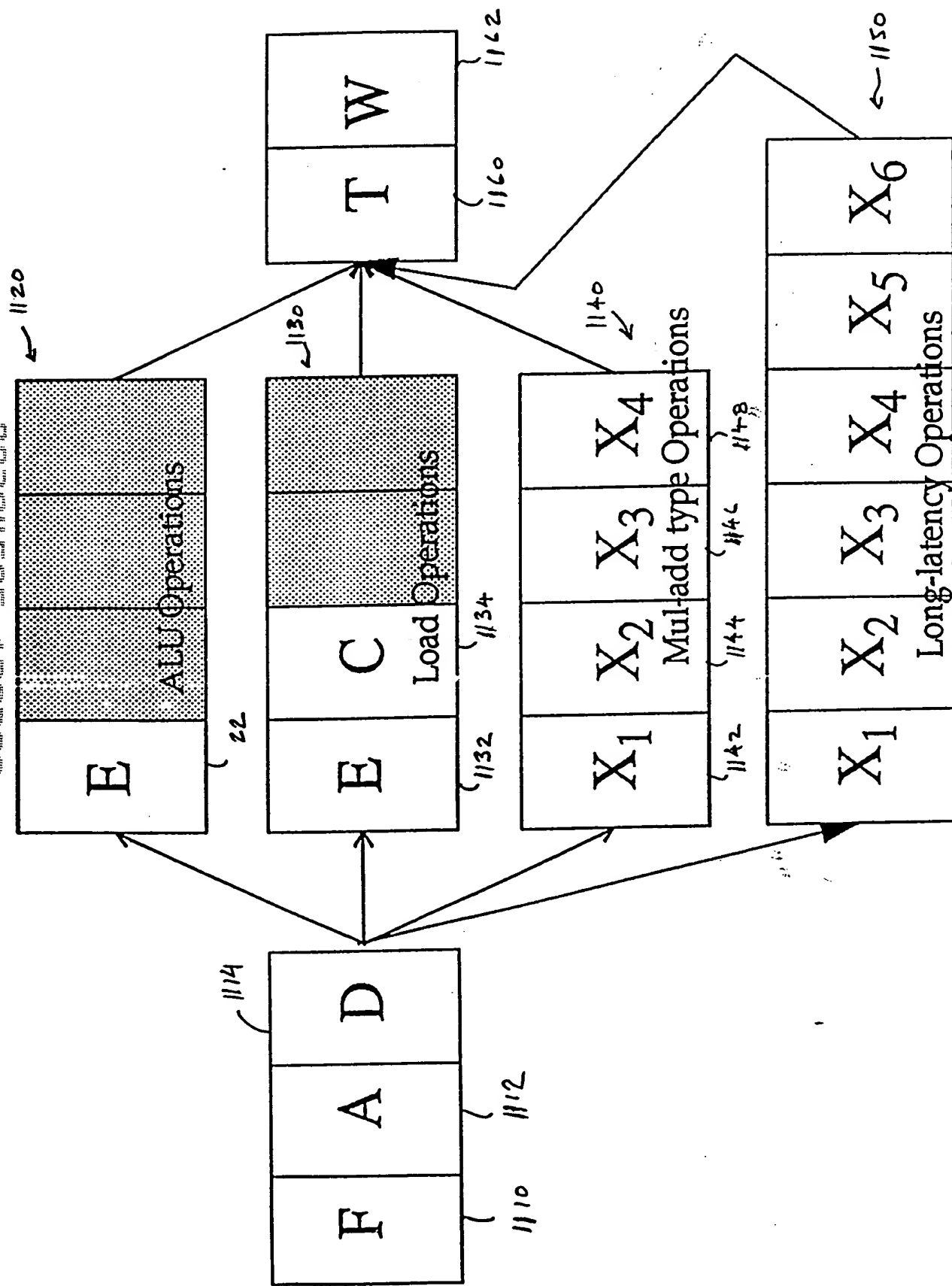


FIGURE 10. Pipeline diagram.